DOCKET NO: MSFT-1117 PATENT

#2 Revo. 2/2/

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Official

RECEIVED
JAN 3 1 2002
Technology Center 2600

In re patent application of:

Fabian W. Meier and Francis S. Bernard

Serial No.: 09/628,658 Group Art Unit: 2753

Filing Date: July 28, 2000 Examiner: Not Assigned Yet

For: System and Method for Storing Compressed Data onto a Storage Medium

BOX DSD
Commissioner of Patents & Trademarks
Washington, DC 20231

Sir:

POWER OF ATTORNEY WITH REVOCATION
AND STATEMENT UNDER 37 CFR 3.73(b)

Revoking any and all powers of attorney heretofore given in the matter of the above-entitled application, the undersigned, assignees of the entire interest in the above-identified application, hereby appoints **STEVEN J. ROCCI**, Registration No. **30,489**, of the firm of WOODCOCK WASHBURN LLP, One Liberty Place - 46th Floor, Philadelphia, Pennsylvania, 19103, as attorney(s) for applicant, with full power of substitution and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith. The authority granted herein shall remain in effect unless otherwise revoked in writing by the undersigned or otherwise terminated.

In addition, the assignee also appoints the following attorneys listed below of **MICROSOFT CORPORATION, One Microsoft Way, Redmond, Washington 98052** with full power of substitution and revocation, to prosecute this application, to make alterations and

amendments therein, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith.

Katie E. Sako	**Registration No. 32,628**
Daniel D. Crouse	**Registration No. 32,022**

Send all future correspondence and address all telephone calls to:

STEVEN J. ROCCI
WOODCOCK WASHBURN LLP
One Liberty Place - 46th Floor
Philadelphia, PA 19103
(215) 568-3100

STATEMENT UNDER 37 C.F.R. '3.73(b)

MICROSOFT CORPORATION, a corporation of the **State of Washington**, certifies that it is the assignee of the entire right, title, and interest in the patent application/patent identified above by virtue of:

☐ An assignment from the inventor(s) of the patent application/patent identified above.

☐ 1. The assignment was recorded in the Patent and Trademark Office at Reel _____ Frame _____.

☐ 2. The assignment has not yet been recorded. A copy of the assignment is attached.

☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

From the inventors to: Silicon Graphics, Inc. The document was recorded in the U.S. Patent and Trademark Office on July 28, 2000, at Reel 010975, Frame 0018. A copy thereof is attached.

From: **Silicon Graphics, Incorporated** To: **Microsoft Corporation.** A copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

☒ Copies of assignments or other documents in the chain of title are attached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignees identified above. The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee, **MICROSOFT CORPORATION**.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: 1/21/02

Name: DANIEL D. CROUSE
Title: Assistant Secretary
Corporation: MICROSOFT CORPORATION

45100

MSFT-1117

FILE COPY

UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office
ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

SEPTEMBER 25, 2000

PTAS

101428530A

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C
MICHAEL B. RAY
1100 NEW YORK AVE., N.W.
SUITE 600
WASHINGTON, D.C. 20005-3934

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 07/28/2000

REEL/FRAME: 010975/0018
NUMBER OF PAGES: 5

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:
MEIER, FABIAN W.

DOC DATE: 07/25/2000

ASSIGNOR:
BERNARD, FRANCIS S.

DOC DATE: 07/26/2000

ASSIGNEE:
SILICON GRAPHICS, INC.
1600 AMPHITHEATRE PARKWAY
MAIL STOP 40-1-710
MOUNTAIN VIEW, CALIFORNIA
94043-
1351

SERIAL NUMBER: 09628658
PATENT NUMBER:

FILING DATE: 07/28/2000
ISSUE DATE:

OCT - 2 2000

10/2
10/0 AGJ

MAURICE CARTER, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS

08-10-2000

Form PTO-1595
6-93
7-28-00

101428530

U.S. Department of Commerce
Patent and Trademark Office

JC864 U.S. PTO
09/628658
07/28/00

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
(1) Fabian W. Meier; and (2) Francis S. Bernard

Additional name(s) of conveying party(ies) attached? ☐ yes ☒ no

2. Name and address of receiving party(ies):

Name: Silicon Graphics, Inc.

Street Address: 1600 Amphitheatre Parkway
Mail Stop 40-1-710

City: Mountain View State: CA Zip Code: 94043-1351

Country: U.S.A.

Additional name(s) & address(es) attached? ☐ yes ☒ no

3. Nature of Conveyance:

☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other _____

Execution Date(s): (1) 7/25/00; (2) 7/26/00

4. Application number(s) or patent number(s): 09629658

If this document is being filed together with a new application, the execution date of the application is

A. Patent Application No(s).
To be assigned

B. Patent No(s).

Additional numbers attached? ☐ yes ☒ no

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: **Sterne, Kessler, Goldstein & Fox P.L.L.C.**

Internal Address: c/o Michael B. Ray

Street Address: 1100 New York Ave., N.W.
Suite 600

City: Washington State: D.C. Zip Code: 20005-3934

6. Total number of applications and patents involved
1

7. Total fee (37 C.F.R. § 3.41).....$ 40.00

☒ Enclosed

☐ Authorized to be charged to Deposit Account

8. Deposit Account Number: 19-0036

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Michael B. Ray
Name of Person Signing
Registration No. 33,997

Signature

7/28/00
Date

Total number of pages including cover sheet, attachments and document 5

OMB NO. 0651-0011 (exp.4/94)

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents and Trademarks, Box Assignments
Washington, D.C. 20231

P:\USERS\AMACWILL\wbedge\1452\3080000\record 017.pto
SKGF Rev 1/26/98 dcw

ASSIGNMENT

In consideration of the sum of One Dollar ($1.00) or equivalent and other good and valuable consideration paid to each of the undersigned inventor(s): (1) Fabian W. Meier; and (2) Francis S. Bernard, the undersigned inventor(s) hereby sell(s) and assign(s) to Silicon Graphics, Inc. (the Assignee) his/her entire right, title and interest, including the right to sue for past infringement and to collect for all past, present and future damages:

check applicable box(es) ☒ for the United States of America (as defined in 35 U.S.C. § 100),
☒ and throughout the world,

(a) in the invention(s) known as System and Method for Storing Compressed Data onto a Storage Medium for which application(s) for patent in the United States of America has (have) been executed by the undersigned on (1)_____; (2) 7/26/00 (also known as United States Application No. to be assigned, filed herewith, in any and all applications thereon, in any and all Letters Patent(s) therefor, and MRK 7/28/00

(b) in any and all applications that claim the benefit of the patent application listed above in part (a), including continuing applications, reissues, extensions, renewals and reexaminations of the patent application or Letters Patent therefor listed above in part (a), to the full extent of the term or terms for which Letters Patents issue, and

(c) in any and all inventions described in the patent application listed above in part (a), and in any and all forms of intellectual and industrial property protection derivable from such patent application, and that are derivable from any and all continuing applications, reissues, extensions, renewals and reexaminations of such patent application, including, without limitation, patents, applications, utility models, inventor's certificates, and designs together with the right to file applications therefor; and including the right to claim the same priority rights from any previously filed applications under the International Agreement for the Protection of Industrial Property, or any other international agreement, or the domestic laws of the country in which any such application is filed, as may be applicable;

all such rights, title and interest to be held and enjoyed by the above-named Assignee, its successors, legal representatives and assigns to the same extent as all such rights, title and interest would have been held and enjoyed by the Assignor had this assignment and sale not been made.

The undersigned inventor(s) agree(s) to execute all papers necessary in connection with the application(s) and any continuing (continuation, divisional, or continuation-in-part), reissue, reexamination or corresponding application(s) thereof and also to execute separate assignments in connection with such application(s) as the Assignee may deem necessary or expedient.

The undersigned inventor(s) agree(s) to execute all papers necessary in connection with any interference or patent enforcement action (judicial or otherwise) related to the application(s) or any continuing (continuation, divisional, or continuation-in-part), reissue or reexamination application(s) thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference or patent enforcement action.

The undersigned inventor(s) hereby represent(s) that he/she has full right to convey the entire interest herein assigned, and that he/she has not executed, and will not execute, any agreement in conflict therewith.

The undersigned inventor(s) hereby grant(s) Douglas J. Crisman, Esquire, Registration No. 39,951 of SILICON GRAPHICS, INC., 1600 Amphitheatre Parkway, Mail Stop 40-1-710, Mountain View, CA 94043-1351; Robert Greene Sterne, Esquire, Registration No. 28,912; Edward J. Kessler, Esquire, Registration No. 25,688; Jorge A. Goldstein, Esquire, Registration No. 29,021; Samuel L. Fox, Esquire, Registration No. 30,353; David K.S. Cornwell, Esquire, Registration No. 31,944; Robert W. Esmond, Esquire, Registration No. 32,893; Tracy-Gene

G. Durkin, Esquire, Registration No. 32,831; Michele A. Cimbala, Esquire, Registration No. 33,851; Michael B. Ray, Esquire, Registration No. 33,997; Robert E. Sokohl, Esquire, Registration No. 36,013; Eric K. Steffe, Esquire, Registration No. 36,688; Michael Q. Lee, Esquire, Registration No. 35,239; Steven R. Ludwig, Esquire, Registration No. 36,203; Raz E. Fleshner, Esquire, Registration No. 34,331; John M. Covert, Esquire, Registration No. 38,759; and Linda E. Alcorn, Esquire, Registration No. 39,588; all of STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C., 1100 New York Avenue, N.W., Suite 600, Washington, D.C. 20005-3934, power to insert in this assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

IN WITNESS WHEREOF, executed by the undersigned inventor(s) on the date opposite his/her name.

Date: _____ Signature of Inventor: _____
Fabian W. Meier

Date: 7/26/00 Signature of Inventor: Francis S. Bernard
Francis S. Bernard

P:\USERS\AMACWILL\wbedge\1452\3080000\assign

ASSIGNMENT

In consideration of the sum of One Dollar ($1.00) or equivalent and other good and valuable consideration paid to each of the undersigned inventor(s): (1) Fabian W. Meier; and (2) Francis S. Bernard, the undersigned inventor(s) hereby sell(s) and assign(s) to Silicon Graphics, Inc. (the Assignee) his/her entire right, title and interest, including the right to sue for past infringement and to collect for all past, present and future damages:

check applicable box(es) ☒ for the United States of America (as defined in 35 U.S.C. § 100),
☒ and throughout the world,

(a) in the invention(s) known as System and Method for Storing Compressed Data onto a Storage Medium for which application(s) for patent in the United States of America has (have) been executed by the undersigned on (1) 7/25/2000; (2) _____ (also known as United States Application No. to be assigned, filed herewith, in any and all applications thereon, in any and all Letters Patent(s) therefor, and

(b) in any and all applications that claim the benefit of the patent application listed above in part (a), including continuing applications, reissues, extensions, renewals and reexaminations of the patent application or Letters Patent therefor listed above in part (a), to the full extent of the term or terms for which Letters Patents issue, and

(c) in any and all inventions described in the patent application listed above in part (a), and in any and all forms of intellectual and industrial property protection derivable from such patent application, and that are derivable from any and all continuing applications, reissues, extensions, renewals and reexaminations of such patent application, including, without limitation, patents, applications, utility models, inventor's certificates, and designs together with the right to file applications therefor; and including the right to claim the same priority rights from any previously filed applications under the International Agreement for the Protection of Industrial Property, or any other international agreement, or the domestic laws of the country in which any such application is filed, as may be applicable;

all such rights, title and interest to be held and enjoyed by the above-named Assignee, its successors, legal representatives and assigns to the same extent as all such rights, title and interest would have been held and enjoyed by the Assignor had this assignment and sale not been made.

The undersigned inventor(s) agree(s) to execute all papers necessary in connection with the application(s) and any continuing (continuation, divisional, or continuation-in-part), reissue, reexamination or corresponding application(s) thereof and also to execute separate assignments in connection with such application(s) as the Assignee may deem necessary or expedient.

The undersigned inventor(s) agree(s) to execute all papers necessary in connection with any interference or patent enforcement action (judicial or otherwise) related to the application(s) or any continuing (continuation, divisional, or continuation-in-part), reissue or reexamination application(s) thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference or patent enforcement action.

The undersigned inventor(s) hereby represent(s) that he/she has full right to convey the entire interest herein assigned, and that he/she has not executed, and will not execute, any agreement in conflict therewith.

The undersigned inventor(s) hereby grant(s) Douglas J. Crisman, Esquire, Registration No. 39,951 of SILICON GRAPHICS, INC., 1600 Amphitheatre Parkway, Mail Stop 40-1-710, Mountain View, CA 94043-1351; Robert Greene Sterne, Esquire, Registration No. 28,912; Edward J. Kessler, Esquire, Registration No. 25,688; Jorge A. Goldstein, Esquire, Registration No. 29,021; Samuel L. Fox, Esquire, Registration No. 30,353; David K.S. Cornwell, Esquire, Registration No. 31,944; Robert W. Esmond, Esquire, Registration No. 32,893; Tracy-Gene

G. Durkin, Esquire, Registration No. 32,831; Michele A. Cimbala, Esquire, Registration No. 33,851; Michael B. Ray, Esquire, Registration No. 33,997; Robert E. Sokohl, Esquire, Registration No. 36,013; Eric K. Steffe, Esquire, Registration No. 36,688; Michael Q. Lee, Esquire, Registration No. 35,239; Steven R. Ludwig, Esquire, Registration No. 36,203; Raz E. Fleshner, Esquire, Registration No. 34,331; John M. Covert, Esquire, Registration No. 38,759; and Linda E. Alcorn, Esquire, Registration No. 39,588; all of STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C., 1100 New York Avenue, N.W., Suite 600, Washington, D.C. 20005-3934, power to insert in this assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

IN WITNESS WHEREOF, executed by the undersigned inventor(s) on the date opposite his/her name.

Date: 7/25/2000 Signature of Inventor: F. Meier

Fabian W. Meier

Date: _____ Signature of Inventor: _____

Francis S. Bernard

P:\USERS\AMACWILL\wbedge\1452\3080000\assign

ASSIGNMENT

WHEREAS, SILICON GRAPHICS, INC., a Delaware Corporation, (hereinafter referred to as "Assignor") owns all right, title and interest in and to the inventions, patents and patent applications (hereinafter referred to as the "Intellectual Property") identified in Schedule A attached hereto; and

WHEREAS, MICROSOFT CORPORATION, a Washington Corporation, (hereinafter referred to as "Assignee"), is desirous of acquiring the entire domestic and foreign right, title, and interest in and under the Intellectual Property.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor assigns and transfers to the Assignee and the Assignee's legal representatives, successors and assigns, pursuant to the terms of a concurrently executed Confidential Patent Assignment Agreement, its full and exclusive rights in and to the Intellectual Property in the U.S. and every foreign country and its entire right, title, and interest in and to the Intellectual Property and related applications (e.g., provisional applications, non-provisional applications, continuations, continuations-in-part, divisionals, reissues, reexaminations, National phase applications, including petty patent applications, and utility model applications) that may be filed in the United States and every foreign country on the Intellectual Property, and extensions or derivations thereof, both foreign and domestic, that may issue thereon, and we do hereby authorize and request the Commissioner of Patents to issue U.S. patents to the above-mentioned Assignee agreeably with the terms of this assignment document.

ASSIGNOR HEREBY AUTHORIZES the Assignee to insert in Schedule A to this assignment document the filing date and application number of any application if the date and number are unavailable at the time this document is executed.

UPON SAID CONSIDERATION, Assignor conveys to the Assignee the right to make application in its own behalf for protection of the Intellectual Property in the U.S. and countries foreign to the U.S. and to claim under the Patent Cooperation Treaty, the International Convention and/or other international arrangement for any such application the date of any earlier U.S. application (or any other application on the invention) to gain priority with respect to other applications.

IN WITNESS WHEREOF, Assignor has caused one of its officers to hereunder set his hand on the date shown below.

Date 9/28/01

Sandra Escher

Senior Vice President
Title

STATE OF Washington)
) SS:
COUNTY OF King)

On this 28th day of September, 2001, before me, a Notary Public in and for said county, appeared , who is personally known to me to be the same person whose name is subscribed to the foregoing assignment document, and acknowledged that he/she signed and delivered the document as his/her free and voluntary act for the uses and purposes therein set forth.

{SEAL}

MELISSA MAEL NOTARY PUBLIC COMMISSION EXPIRES 10-9-01 STATE OF WASHINGTON

Melissa Mael
Notary Public

My Commission Expires: 10-9-01

Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
Digital Media	US	5,506,624	Rotating Sample of Video Images	4/9/1996
	US	5,745,713	Movie-Based Facility for Launching Application Programs or Services	4/28/1998
	US	5,774,666	System and Method for Displaying Uniform Network Resource Locators Embedded in Time-Based Medium	6/30/1998
	US	5,987,509	System and Method for Displaying an Active URL During Playback of a Media File or Media Broadcast	11/16/1999
	US	5,808,662	Synchronized, Interactive Playback of Digital Movies Across a Network	9/14/1998
	US	5,943,347	Apparatus and Method for Error Concealment in an Audio Stream	8/24/1999
	US	6,005,600	High Performance Player for Distributed, Time-Based Media	12/21/1999
	US	6,147,695	System and Method for Combining Multiple Video Streams	11/14/2000
	US	6,075,906	System and Method for the Scaling of Image Streams that Use Motion Vectors	6/13/2000
	US	D385,585	Video Camera Used with Personal Computer	12/26/1995
	US	5,604,866	Flow Control System Having a Counter in Transmitter for Decrementing and Incrementing Based Upon Transmitting and Received Message Size Respectively for Indicating Free Space in Receiver	2/18/1997
	US	5,311,329	Digital Filtering for Lenticular Printing	5/10/1994
	US	5,438,428	Digital Filtering for Lenticular Printing	8/1/1995
	US	6,070,002	System Software for Use in a Graphics Computer System Having a Shared System Memory	3/30/2000
	US	5,649,186	System and Method for a Computer-Based Dynamic Information Clipping Service	
Internet	US	5,737,560	Graphical Method and System for Accessing Information on a Communications Network	4/7/1998
	US	5,877,767	Graphical Method and System for Accessing Information on a Communications Network	3/2/1999
	US	5,742,768	System and Method for Providing and Displaying a Web Page Having an Embedded Menu	
	US	5,890,170	Method and Apparatus for Publishing Documents in a Hypertextual Network Environment	3/30/1999
	US	6,026,433	Method of Creating and Editing a Web Site in a Client-Server Environment Using Customizable Web Site Templates	2/15/2000
	US	6,072,491	Method and Computer Program Product for Accessing a Web Site	6/6/2000
	US	6,096,096	Web-Site Delivery	8/1/2000
	US	6,098,092	Server to Dynamically Generate Graphics for the World Wide Web	8/1/2000
	US	6,189,029	Web Survey Tool Builder and Result Compiler	2/13/2001
	US	6,081,829	General Purpose Web Annotations Without Modifying Browser	6/27/2000
	US	6,199,098	Method and Apparatus for Providing an Expandable, Hierarchical Index in a Hypertextual, Client-Server Environment	3/6/2001
	US	6,012,055	Mechanism for Integrated Information Search and Retrieval from Diverse Sources Using Multiple Navigation Methods	1/4/2000
Graphics Rendering	US	4,772,881	Pixel Mapping Apparatus for Color Graphics Display	9/20/1988
	US	5,038,297	Method and Apparatus for Clearing a Region of Z-Buffer	8/6/1991
	US	5,197,126	Clock Switching Circuit for Asynchronous Clocks of Graphics Generation Apparatus	3/23/1993

Schedule A - U.S. Patents

Country	Patent No.	Title	Issue Date
US	4,771,279	Dual Clock Shift Register	9/13/1988
US	4,951,232	Method for Updating Pipelined Single Port Z-Buffer by Segments on a Scan Line	8/21/1990
US	4,789,927	Interleaved Pipeline Parallel Processing Architecture	12/6/1988
US	5,113,490	Method for Forming a Computer Model from an Intersection of a Cutting Surface with a Bounded Volume	5/12/1992
US	5,182,548	Method and Apparatus for Painting on a Computer	1/26/1993
US	4,991,110	Graphics Processor with Staggered Memory Timing	2/5/1991
US	5,129,059	Graphics Processor with Staggered Memory Timing	7/7/1992
US	5,193,145	Method and Apparatus for Producing a Visually Improved Image in a Computer System	3/9/1993
US	5,847,700	Integrated Apparatus for Displaying a Plurality of Modes of Color Information on a Computer Output Display	12/8/1998
US	5,371,518	Video Timing and Display ID Generator	12/6/1994
US	5,621,432	Method and Apparatus for Generating Display Identification Information	4/15/1997
US	5,051,737	Efficient Graphics Process for Clipping Polygons	9/24/1991
US	5,266,941	Apparatus and Method for Controlling Storage of Display Information In a Computer System	11/30/1993
US	5,394,170	Apparatus and Method for Controlling Storage of Display Information In a Computer System	2/28/1995
US	5,768,552	Graphical Representation of Computer Network Topology and Activity	6/16/1998
US	5,585,824	Graphics Memory Apparatus and Method	12/17/1996
US	5,818,439	Graphics Memory Apparatus and Method	10/6/1998
US	5,307,450	Z-Subdivision for Improved Texture Mapping	4/26/1994
US	5,230,039	Texture Range Controls for Improved Texture Mapping	7/20/1993
US	5,343,558	Method for Scan Converting Shaded Triangular Polygons	8/30/1994
US	5,347,618	Method for Display Rendering by Determining the Coverage of Pixels in Polygons	9/13/1994
US	5,345,252	High Speed Cursor Generation Apparatus	9/6/1994
US	5,265,199	Method and Apparatus for Accomplishing Z-Buffering by Prediction	11/23/1993
US	5,548,709	An Apparatus and Method for Integrating Texture Memory and Interpolation Logic In a Computer System	8/20/1996
US	5,706,481	Apparatus and Method for Integrating Texture Memory and Interpolation Logic In a Computer System	1/6/1998
US	5,684,939	Antialiased Imaging with Improved Pixel Supersampling	11/4/1997
US	6,072,500	Antialiased Imaging with Improved Pixel Supersampling	6/6/2000
US	5,581,680	Method and Apparatus for Antialiasing Raster Scanned Images	12/3/1996
US	5,836,338	Method for Designing Curved Shapes for Use by a Computer	6/3/1997
US	5,515,484	Method and Apparatus for Rendering Volumetric Images	5/7/1996
US	5,369,739	Apparatus and Method for Generating Point Sample Masks In a Graphics Display System	11/29/1994
US	5,528,737	Processor-Based Method for Rasterizing Polygons at an Arbitrary Precision	6/18/1996
US	5,805,782	A Method and Apparatus for Projective Texture Mapping Rendered from Arbitrarily Positioned and Oriented Light Source	9/8/1998
US	5,682,554	Apparatus and Method for Handling Data Transfer Between a General Purpose Computer and a Cooperating Processor	10/28/1997

Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
	US	5,457,779	System for Accessing Graphic Data in a SIMD Processing Environment	10/10/1995
	US	5,671,401	Apparatus for Efficiently Accessing Graphic Data for Rendering on a Display	9/23/1997
	US	5,742,749	A Method and Apparatus for Shadow Generation Through Depth Mapping	4/21/1998
	US	5,490,240	A System and Method of Generating Interactive Computer Graphic Images Incorporating Three Dimensional Textures	2/6/1996
	US	5,438,654	System and Method for Sharpening Texture Imagery in Computer Generated Interactive Graphics	8/1/1995
	US	5,455,827	Programmable Video Output Format Generator	10/3/1995
	US	5,471,572	A System and Method for Adding Detail to Textures Imagery in Computer Generated Interactive Graphics	11/28/1995
	US	5,704,024	A Method & Apparatus for Generating Reflection Vectors Which can be Unnormalized and For Using These Reflection Vectors to Index Locations on an Environment Map	12/30/1997
	US	5,649,082	An Efficient Algorithm for Computer Texture Coordinates for Lines & Polygons	7/15/1997
	US	5,528,738	Method and Apparatus for Antialiasing Raster Scanned, Polygonal Shaped Images	6/18/1996
	US	5,619,597	Method for Sampling a Uniform Spatially-Distributed Sequence of Pixels in a Block	8/8/1997
	US	5,710,876	A Computer Graphics System for Rendering Images Using Full Spectral Illumination Data	1/20/1998
	US	5,835,133	Optical System for Single Camera	11/10/1998
	US	5,608,461	Programmable Video Frame Detector	3/4/1997
	US	5,703,810	Improved DRAM for Texture Mapping	12/30/1997
	US	5,943,058	Texture Mapping Circuit for Performing Data Interpolations	8/24/1999
	US	5,982,939	System and Method for Antialiasing of Texture Edges	11/9/1999
	US	5,844,567	Computer Graphics System and Mehod for Texture Mapping Using Triangular Interpolation	12/1/1998
	US	5,819,017	Apparatus and Method for Selectively Storing Depth Information of a 3-D Image	10/6/1998
	US	5,877,771	A Method and Apparatus for Supersampling Based on the Local Rate of Change in Texture	3/2/1999
	US	5,760,783	A Method and Apparatus for Providing Texture Using a Selected Portion of a Texture MIP-MAP	6/2/1998
	US	5,818,613	System and Method for Color Space Conversion	10/6/1998
	US	5,736,988	Appartus and Method for Accelrated Tiled Data Retrieval	4/7/1998
	US	5,657,479	Hierarchical Display List Processing in Graphics Data Retrieval System	8/12/1997
	US	5,936,626	Computer Graphics Silhouette Load Management	8/10/1999
	US	5,845,874	System and Method for Creating Visual Images of Aircraft Wake Vortices	12/8/1998
	US	6,226,003	Method for Rendering Silhouette and True Edges of 3-D Line Drawings with Occlusion	5/1/2001
	US	6,131,189	Systen and Method to Efficiently Represent Aliases and Indirect Memory Operations in Static Single Assignment Form During Compilation	10/10/2000
	US	5,946,113	System and Method for Color Space Conversion Using an Extended Color Space	8/31/1999
	US	6,147,772	System and Method for Color Space Conversion Using an Extended Color Space	11/14/2000
	US	5,831,620	Sytan and Computer-Based Method for Creating Real-Time Mirror Reflections	11/3/1998

Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
	US	5,815,162	Computational Low-Cost Anti-Aliased Bresenham Line Algorithm	9/29/1998
	US	6,249,289	Multi-Purpose High Resolution Distortion Correction	6/19/2001
	US	5,949,424	Method, System and Computer Program Product for Bump Mapping in Tangent Space	9/7/1999
	US	5,880,736	Method, System and Computer Program Product for Shading	3/9/1999
	US	6,163,319	Method, System and Computer Program Product for Shading	12/19/2000
	US	6,104,417	Unified Memory Computer Architecture with Dynamic Graphics Memory Allocation	8/15/2000
	US	6,154,794	Upstream Situated Apparatus and Method within a Computer System for Controlling Data Flow to a Downstream Situated Input/Output Unit	11/28/2000
	US	6,078,331	A Method and System for Efficiently Drawing Subdivision Surfaces for 3D Graphics	6/20/2000
	US	6,078,332	Real-Time Lighting Algorithm Using 3D Texture Mapping	6/20/2000
	US	6,175,367	A Method and System for Real Time Illumination of Computer Generated Images	1/16/2001
	US	6,002,406	System and Method for Storing and Accessing Data Representative of an Object in Various Levels of Detail	12/14/1999
	US	6,154,215	Method and Apparatus for Maintaining Multiple Representations of a Same Scene in Computer Generated Graphics	11/28/2000
	US	6,057,850	Blended Texture Illumination Mapping	5/2/2000
	US	5,907,962	Latching Assembly for a Computer	6/1/1999
	US	6,215,495	A Platform Independent Application Program Interface for Interactive 3D Scene Management	4/10/2001
	US	6,108,007	Method, Sytem and Computer Program Product for Increasing Interpolation Precision Using Multi-Channel Texture Mapping	8/22/2000
	US	6,104,415	Method for Accelerating Minified Textured Cache Access	8/15/2000
	US	6,232,981	Method for Improving Texture Locality for Pixel Quads by Diagonal Level of Detail Calculation	5/15/2001
	US	6,232,979	Method, System and Computer Program Product for Fast Computation Using Parallel Multi-Channel Resampling and Blending	5/15/2001
	US	6,246,415	Method and Apparatus for Culling Polygons	6/12/2001
	US	6,236,413	Method and System for a RISC Graphics Pipeline Optimized for High Clock Speeds by Using Recirculation	5/22/2001
	US	6,075,546	A Packetized Command Interface to a Graphics Processor	6/13/2000
	US	6,230,177	Method and Apparatus for Performing Fast Fourier Transforms	5/8/2001
	US	6,252,610	Method and Apparatus for Efficiently Switching State in a Graphics Pipeline	6/26/2001
	US	6,128,638	Method and Apparatus for Calculating X to the Exponent of Y	10/3/2000
	US	6,133,901	Method and System for Width Independent Antialiasing	10/17/2000
	US	6,091,425	Constant Multisample Image Coverage Mask	7/18/2000
	US	6,205,531	Method and Apparatus for Virtual Address Translation	3/20/2001
	US	6,256,656	Apparatus and Method for Extending Computational Precision of a Computer System Having a Modular Arithmetic Processing Unit	7/3/2001
	US	6,229,547	System and Method for Rendering Multi-Planar Reformations Using Bi-Linear Interpolation	5/8/2001

Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
	US	6,288,861	Volumetric Three-Dimensional Fog Rendering Technique	7/31/2001
	US	6,163,320	Method and Apparatus for Radiometrically Accurate Texture-Based Lightpoint Tendering Technique	12/19/2000

Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
Tablet Computing	US	196100	"Pen-Based Interface for a Notepad Computer"	11/20/1998
	US	196114	"Pen-Based Computer System"	11/20/1998
Graphics Rendering	US	09/005129	System and Method for the Direct Rendering of Curve Bounded Objects	1/8/1998
	US	191458	Floating Point Gamma Correction Method And System	11/12/1998
	US	09/217398	High Precision Texture Wrapping Method And Device	12/21/1998
	US	265487	Method And Device For Associating A Pixel With One Of A Plurality Of Regions In A Logarithm Or Cosine Space	3/9/1999
	US	072050	Method and System For Providing Texture Using A Selected Portion of a Texture Map	5/5/1998
	US	033663	Improved Chroma-Key Suppression Method and Apparatus	3/3/1998
	US	09/244275	Memory Chip For Use in a Unified Memory Architecture	2/3/1999
	US	244281	Memory Controller for Controlling Memory in a Computer System Having a Unified Memory Architecture	2/3/1999
	US	244254	I/O Chip in a Computer System Having a Unified Memory Architecture	2/3/1999
	US	220078	System and Method for Morphing Based on Multiple Weighted Parameters	12/23/1998
	US	845526	A Method and System for Efficiently Evaluating and Drawing Nurbs Surfaces for 3D Graphics	4/25/1997
	US	899123	System and Method for Displaying Different Portions of an Object in Different LOD Levels	7/23/1997
	US	070808	System and Method for Displaying Different Portions of an Object in Different Levels of Detail	5/1/1998
	US	074027	Occlusion Culling For Complex Transparent Scenes in Computer Generated Graphics	5/8/1998
	US	137005	Method and System for Performing Rasterization in Producing Three-Dimensional Graphics Using YUV Color Space and Combining Same with Digital Video in YUV Color Space	8/20/1998
	US	035376	Subsampled Texture Edge Antialiasing	3/5/1998
	US	956537	Method and Apparatus for Providing Image and Graphics Processing Using a Graphics Rendering Engine	10/23/1997
	US	937793	Method, System and Computer Program Product for Providing Illumination in Computer Graphics Shading and Animation	9/25/1997
	US	09/048099	Method for Efficient Handling of Texture Cache Misses by Recirculation	3/26/1998
	US	09/081073	Method and Apparatus for Line Antialiasing by Gamma-Corrected Area Calculation	5/19/1998
	US	246136	System and Method for Rendering an Image	2/8/1999
	US	09/448907	A Packetized Command Interface to a Graphics Processor	11/23/1999
	US	09/549156	A Packetized Command Interface to a Graphics Processor	4/13/2000
	US	09/145516	Method and Apparatus For Rasterizing in a Hierarchical Tile Order	9/2/1998
	US	978755	Range Correct Layered Fog Model Using 3D Texture	11/26/1997
	US	111284	Backface Primitives Culling	7/6/1998
	US	201814	Multi-Threaded Texture Modulation for Axis Aligned Volume Rendering	12/1/1998
	US	247422	Multisample Dither Method With Exact Reconstruction	2/10/1999

Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	09/227227	Method and Apparatus For Synchronizing Graphics Pipelines	1/8/1999
	US	218121	Scaleable Network Based Computer Graphics System	12/21/1998
	US	346071	Computer System Having A Distributed Texture Memory Architecture	7/1/1999
	US	09/707418	Method and Apparatus for Radiometrically Accurate Texture-Based Lightpoint Rendering Technique	11/6/2000
	US	348882	Antialiasing Method Using Barycentric Coordinates Applied to Lines	7/1/1998
	US	09/220596	System and Method For Transitioning Between Two Filters, Allowing For The Use of Higher Order Interpolation	12/28/1998
	US	09/416055	Method, System, and Computer Program Product For Compositing True Colors and Intensity-Mapped Colors Into A Frame Buffer	10/12/1999
	US	218042	System and Method For Locking Disparate Video Formats	12/22/1998
	US	09/247885	Method For Interfacing to Ultra-High Resolution Output Devices	2/10/1999
	US	232860	Method For Tiling Multiple Displays to Generate a Large Area Display of Moving Data	1/15/1999
	US	294546	Combined Floating-Point Logic Core and Frame Buffer	4/19/1999
	US	09/294450	Apparatus and Method for Sharing Antialiasing Memory Across Multiple Displays	4/19/1999
	US	277567	Transformation Pipeline for the Computing Distortion Correction Geometry for any Design Eyepoint, Display Surface Geometry and Projector Position	3/26/1999
	US	385270	Antialiasing Method for Computer Graphics	3/23/1999
	US	09/220156	Processor for Geometry Transformations and Lighting Calculations	12/23/1998
	US	263185	System and Method for Maintaining Time Dependencies in Conversions That Include Parallel Operations	3/5/1999
	US	09/457561	A Method and System for Efficiently Implementing Two Sided Vertex Lighting in Hardware	12/20/1999
	US	275725	Method and Apparatus for Texture Memory Management	3/24/1999
	US	09/247816	Method and Apparatus for Early Culling of Occluded Objects	2/9/1999
	US	346641	Method and System for Generating Light Values for a Set of Vertices	7/1/1999
	US	273247	Method and System for Dynamic Clock Frequency Adjustment for a Graphics Subsystem in a Computer	3/19/1999
	US	306987	Fine Grain Multi-Pass for Multiple Texture Rendering	5/7/1999
	US	344005	Method and System for Dynamic Texture Replication on a Distributed Memory Graphics Architecture	6/24/1999
	US	345366	Cache Memory For High Latency and Out-of-Order Return of Texture Data	7/1/1999
	US	328000	Method for Virtual Clipping A Three-Dimensional Graphics Image	6/8/1999
	US	09/502497	A Method and System For Efficient Simplification of Tetrahedral Meshes Used In 3D Volumetric Representations	2/11/2000

Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	09/377778	Method, System and Computer Program Product for Multi-Pass Bump-Mapping into an Environment Map	8/20/1999
	US	328184	Method and Apparatus for a Modified Linear Filter Using Texture Data as Phase Angle	6/8/1999
	US	303894	View-Dependent Layer Ordering Method and System	5/3/1999
	US	300916	Method and System for Iterative Morphing	4/28/1999
	US	293869	Apparatus and Method for Increasing the Bandwidth to a Graphics Subsystem	4/19/1999
	US	265493	Device, Method and System for Generating Per-Pixel Light Values Using Texture Parameters	3/9/1999
	US	386379	Method, System, and Computer Program Product for Efficient Buffer Level Management of Memory-Buffered Graphics Data	8/31/1999
	US	386378	Method, System and Computer Program Product for Overlapping Graphics Data Collection and Transmission Using a Single Processor	8/31/1999
	US	220082	Method, System and Computer Program Product for Modified Blending Between Clip-Map Tiles	12/23/1998
	US	09/369359	Reflection Space Image Based Rendering	8/6/1999
	US	09/727985	Texture Generating Apparatus For Dynamic Interference Checking	11/30/2000
	US	09/575979	Method and System for Evaluating Derivatives in Screen Space Using Perspective Corrected Barycentric Coordinates	9/29/2000
	US	363636	Method and System for Transforming Color Coordinates by Direct Calculation	7/30/1999
	US	09/576740	Cheap, Well-Behaved Affine Transformation of Bounding Spheres	5/23/2000
	US	348885	Dual Mode Device and Method for Generating Vector Cross Products or DOT Products	7/1/1999
	US	408951	System and Method for Load Balancing in a Multi-Channel Graphics System	9/30/1999
	US	09/404808	Method, System, and Computer Program Product for Using Alpha Values to Control Pixel Blending	9/24/1999
	US	09/461345	Method, System, and Computer Program Product for Generating Spatically Varying Effects in a Digital Image	12/15/1999
	US	09/585562	Method and Apparatus for Rendering a Quadrangle Primitive	5/4/2000
	US	60/295854	Reducing Fill and Improving Quality of Interlaced Displays Using Multi-Sampling	6/6/2001
	US	09/832138	Scene Representation Method and System	4/10/2001
	US	09/569649	Method and System for Implementing Graphics Control Constructs	5/12/2000
	US	09/569650	Table Indexing System and Method	5/12/2000
	US	09/569521	Method and System for Accelerating Noise	5/12/2000
	US	09/569520	Data Retrieval Method and System	5/12/2000
	US	09/569654	Extended Range Pixel Display Method and System	5/12/2000
	US	09/806988	Method, System and Computer Program Product for Implementing Derivative Operators with Graphics Hardware	6/29/2000
	US	09/638907	Method and System for Executing SIMD Instruction Using Graphics Technology	8/15/2000
	US	09/572452	Method, System, and Computer Program Product for Simulating Camera Depth-of-Field Effects in a Digital Image	5/17/2000
	US	09/469558	An Efficient Graphics Pipeline With a Pixel Cache and Data Pre-Fetching	12/22/1999

Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	09/470948	A System and Method for Linearly Mapping a Tiled Image Buffer	12/22/1999
	US	09/473209	A Graphics Geometry Cache	12/27/1999
	US	09/473210	View Volume Clip-Check In Object Space	12/27/1999
	US	09/684810	Method, System, and A Computer Program Product For Filtering A Texture Applied To A Surface Of A Computer Generated Object	10/10/2000
	US	09/599971	Method and System for Performing Multi-Texturing Operations Using Recursive Interleaving	6/21/2000
	US	09/688978	System and Method for Efficiently Controlling a Graphics Rendering Pipeline	10/17/2000
	US	60/255883	Method, System and Computer Program Product for Determining Regions That Are Occluded From An Observation Point	12/18/2000
	US	09/684812	Method, System, and Computer Program Product for Anistropic Filtering and Applications Thereof	10/10/2000
	US	60/253946	System, Method, and Computer Program Product For General Environment Mapping	11/30/2000
	US	60/258329	Hardware-Accelerated Volume Lighting Algorithm	12/28/2000
	US	60/252094	Rendering Volumetric Fog and Other Gaseous Phenomena	11/21/2000
	US	60/238128	Texture Tiling With Adjacency Information	10/6/2000
	US	60/298416	A Volumetric Based Method and System for Visualizing Datasets	6/18/2001
	US	60/252092	Rendering Volumetric Fog and Other Gaseous Phenomena Using an Alpha Channel	11/21/2000
	US	60/252093	Method, System, and Computer Program Product for Rendering Multicolored Layered Fog with Self-Shadowing and Scene Shadowing	11/21/2000
Internet	US	357529	General Purpose Web Annotations Without Modifying Browser	7/19/1999
	US	09/557149	Graphical Method and System for Accessing Information on a Communications Network	4/25/2000
Digital Media	US	09/415037	System and Method for Media Stream Indexing and Synchronization	10/7/1999
	US	09/632558	Workstation for Processing and Producing a Video Signal	8/4/2000
	US	09/632662	System and Method for Pre-Processing a Video Signal	8/4/2000
	US	09/632462	System and Method for Producing a Video Signal	8/4/2000
	US	09/632451	System and Method for Packing and Unpacking Video Data	8/4/2000
	US	08/900421	Synchronizing Motion and Time-Based Data for Transfer Between a Server and a Client	7/25/1997
	US	09/441729	Seamless Payback of Multiple Clips of Media Data Across a Data Network	11/16/1999
	US	09/441722	Frame-Accurate Transport of Media Data Across a Data Network	11/16/1999
	US	09/020072	Backfilling Network Movie Player	2/6/1998
	US	035687	Preemptive Time Multiplexed Shared Memory Access	3/5/1998
	US	09/427197	Video Assistance System with Computer Generated Imagery Overlay	10/25/1999
	US	09/099742	System and Method Using a Packetized Encoded Bitstream for Parallel Compression and Decompression	6/18/1998
	US	09/519309	A Method and System for Efficiently Streaming 3D Animation Across a Wide Area Network	3/6/2000
	US	09/466398	Constant Bitrate Algorithm for Block Based Image Compression	12/17/1999
	US	09/539365	System and Method for Communicating Video Data in a Digital Media Device	3/31/2000

Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	09/577171	Cost-Optimal Convolution Algorithm With Low or Null Latency	5/23/2000
	US	09/544360	A Technique For Controlling Media Data Streams Using Embedded Controls	4/6/2000
	US	09/521234	Apparatus and Method for Recognizing Color Space of a Digital Video Input	3/8/2000
	US	09/628796	System and Method for Compressing Data	7/28/2000
	US	09/628658	System and Method for Storing Compressed Data onto a Storage Medium	7/28/2000
	US	60/281105	Method and Apparatus for Producing Digital Video from a General Digital Graphics Interface	

Schedule A - Foreign Patents and Applica

Country	Appl/Patent No.	Title	Foreign Filing Date	Issue Date	Status
EPO	98949509.8	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Abandoned
Japan	2000-513245	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Pending
PCT	US98/20096	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Pending
France	99942475.7	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	5/17/2000		Pending
Japan	2000-568059	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	8/2/2000		Pending
PCT	US99/19353	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	8/23/1999		Pending
UK	99942475.7	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	5/17/2000		Pending
EPO	903207.9	Method And Apparatus For Synchronizing Graphics Pipelines	7/11/2001		Pending
Japan		Method And Apparatus For Synchronizing Graphics Pipelines	7/9/2001		Pending
PCT	US00/00548	Method And Apparatus For Synchronizing Graphics Pipelines	1/7/2000		Pending
PCT	US00/10634	Combined Floating-Point Logic Core And Frame Buffer	4/19/2000		Pending
PCT	US00/107904	Method And Apparatus For Texture Memory Management	3/24/2000		Pending
PCT	US00/10379	Apparatus And Method For Increasing The Bandwidth To A Graphics Subsystem	4/19/2000		Pending
PCT	US00/06184	Device, Method And System For Generating Per-Pixel Light Values Using Texture Parameters	3/8/2000		Abandoned
PCT	US00/40788	Method, System And Computer Program Product For Overlapping Graphics Data Collection And Transmission Using A Single Processor	8/31/2000		Pending
PCT	US99/29984	Method, System And Computer Program Product For Modified Blending Between Clip-Map Tiles	12/17/1999		Pending
PCT	US00/21361	Workstation For Processing And Producing A Video Signal	8/4/2000		Abandoned
PCT	US00/21362	System And Method For Pre-Processing A Video Signal	8/4/2000		Abandoned
PCT	US00/21363	System And Method For Producing A Video Signal	8/4/2000		Abandoned
PCT	US00/21360	System And Method For Packing And Unpacking Video Data	8/4/2000		Abandoned
EPO	99909783.7	Preemptive Time Multiplexed Shared Memory Access	3/2/1999		Pending
Japan	2000-534947	Preemptive Time Multiplexed Shared Memory Access	3/2/1999		Pending
PCT	US99/04815	Preemptive Time Multiplexed Shared Memory Access	3/2/1999		Pending
PCT		System And Method For Compressing Data	7/26/2001		Pending
PCT		System And Method For Storing Compressed Data Onto A Storage Medium	7/27/2001		Pending
Canada	1290870	Pixel Mapping Apparatus For Color Graphics		10/15/1991	Granted
Germany	DE3736195	Pixel Mapping Apparatus For Color Graphics		11/27/1997	Granted
Hong Kong	527	Pixel Mapping Apparatus For Color Graphics		6/3/1999	Granted
Japan	2913096	Pixel Mapping Apparatus For Color Graphics		4/16/1999	Granted
Singapore	2193319	Pixel Mapping Apparatus For Color Graphics		12/26/1991	Granted
UK	2193319	Pixel Mapping Apparatus For Color Graphics		5/29/1991	Granted
India	172927	Dual Clock Shift Register		5/27/1994	Granted
EPO	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/29/1996	Granted
France	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/29/1996	Granted
Germany	DE68926571	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/29/1996	Granted

Schedule A - Foreign Patents and Applicatio

Country	Appl/Patent No.	Title	Foreign Filing Date	Issue Date	Status
Japan	2884250	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		2/12/1999	Granted
Korea	131820	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		12/4/1997	Granted
Switzerland	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/28/1996	Granted
Canada	1276312	Interleaved Pipeline Parallel Processing Architecture		11/13/1990	Granted
EPO	95912789.8	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		Pending
PCT	US95/02853	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		Abandoned
JP	9510309	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		
EPO	96926193	Apparatus And Method For Selectively Storing Depth Information Of A 3-D Image	8/15/1995		Abandoned
PCT	US96/13245	Apparatus And Method For Selectively Storing Depth Information Of A 3-D Image	8/15/1995		Abandoned
EPO	96939541.7	A Method And Apparatus For Providing Texture Using A Selected Portion Of A Texture Mip-Map	11/8/1996		Abandoned
PCT	US96/17873	A Method And Apparatus For Providing Texture Using A Selected Portion Of A Texture Mip-Map	11/8/1996		Abandoned
EPO	97917591.6	System And Method For Color Space Conversion Using An Extended Color Space	3/28/1997		Abandoned
PCT	US97/04691	System And Method For Color Space Conversion Using An Extended Color Space	3/28/1997		Abandoned
Canada	2214868	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	9/9/1997		Pending
EPO	97308897.6	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	9/5/1997		Pending
Japan	251134.97	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	9/18/1997		Pending
Mexico	976495	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	8/28/1997		Pending
PCT	US99/15075	Method And Apparatus For Virtual Address Translation	7/1/1999		Abandoned
PCT	US99/19502	System and Method for Combining Multiple Video Streams	8/26/1999		Abandoned
PCT	US96/8946	Digital Filtering for Lenticular Printing	8/18/1992		Abandoned
AU	9225485	Digital Filtering for Lenticular Printing	8/18/1992		Abandoned
PCT	US89/3924	Method and Apparatus for Clearing a Region of Z-Buffer	9/12/1989		Abandoned
EP	551251	Method and Apparatus for Clearing a Region of Z-Buffer	9/12/1989	2/7/1996	Abandoned
DE	68925851	Method and Apparatus for Clearing a Region of Z-Buffer	9/12/1989	3/21/1996	Abandoned
PCT	WO90/02991	Graphics Processor with Staggered Memory Timing	published 3/22/1990		Abandoned
PCT	WO91/12588	Method and Apparatus for Producing a Visually Improved Image in a Computer System	published 8/22/1991		Abandoned
AU	9173134	Method and Apparatus for Producing a Visually Improved Image in a Computer System	published 9/3/1991		Abandoned
PCT	US00/18745	Computer System Having A Distributed Texture Memory Architecture	6/16/2000		Pending